

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of

SHUNPEI YAMAZAKI

Serial No. 09/433,705

Filed: 11/04/1999

For: METHOD OF FABRICATING A
SEMICONDUCTOR DEVICE



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Group Art Unit: 2811
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Examiner: S. Loke
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#14
11-13-01
Fayton

INFORMATION DISCLOSURE STATEMENT AND
NOTIFICATION OF RELATED APPLICATIONS

Honorable Commissioner of Patents

Washington, D.C. 20231

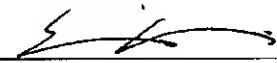
Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The Examiner's attention is directed to related Application Serial Nos. 08/922,363, 09/526,486 and 09/526,487 which are listed on the Form PTO-1449 submitted herewith.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,


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EJR/amd